IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:

Gerard CHAUVEL et al.

Confirmation No.:

Unknown

Serial No.:

Not Yet Assigned

Group Art Unit:

Unknown

Filed:

Concurrently Herewith

Examiner:

Unknown

For:

Cache Coherency In A

Multi-Processor System

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P. O. Box 1450 Alexandria, VA 22313-1450

Att'y. Docket No. TI-35637 CRT Docket No. 1962-05421

Sir:

This Information Disclosure Statement comprises a list of pertinent art of which Applicants are aware. A copy of the publications listed on Form PTO-1449 are enclosed herewith.

The submission of this Information Disclosure Statement is not an admission that the art cited is "prior" with respect to the present invention nor is it a representation that no better art exists. Applicants hereby reserve the right to swear behind or otherwise disprove any alleged "prior" nature of any art cited should the facts support and the situation warrant such an action. It is submitted that the art cited does not constitute a bar to the patentability of Applicants' invention.

This Information Disclosure Statement is being filed concurrently with the patent application for the above-identified matter. Therefore, no fee is due for the filing of these papers.

Respectfully submitted,

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FORM PTO-1449 U.S. DEPARTMENT OF COMMERCE (REV. 7-80) PATENT AND TRADEMARK OFFICE					ATTY. DOCKET NO. TI-35637 (1962-05421)		SERIAL NO. NOT YET ASSIGNED		
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